

1 CONTROLLING FLOW OF DATA BETWEEN DATA PROCESSING SYSTEMS VIA  
2 A MEMORY

3 ABSTRACT

4 Apparatus, methods and systems are described for controlling flow of data between first  
5 and second data processing systems via a memory. In an example embodiment, the  
6 apparatus comprises descriptor logic for generating a plurality of descriptors including a  
7 frame descriptor defining a data packet to be communicated between a location in the  
8 memory and the second data processing system, and a pointer descriptor identifying the  
9 location in the memory. The apparatus also comprises a descriptor table for storing the  
10 descriptors generated by the descriptor logic for access by the first and second data  
11 processing system.